

# Meeting 9: Register Allocation

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## Announcements

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- Exercise 2 due Thursday before class
- HW3 due Friday 9/29 **at 6pm**

## HW1 Comments

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Hours spent: mean 11.9 hours, stdev 6.2, median 10

How hard (out of 6): mean 3.8, stdev 1.1, median 4

- "It took a while to get started in any kind of productive way. ... After a while I understood why. I had a lot of fun implementing the compiler. It struck a really good balance of being challenging but very doable"
- "I really like the freedom this class gives for how to design the compiler, but some nudges towards a reasonable setup would be nice so we don't dig ourselves into a hole."
- "Just implementing it -- conceptually the assignment was easy to think about, but it was tedious to implement, and our own syntax bugs were the main issues we came across"
- "I like the discussion based aspect so far, however, I would like if Chang would cover either important info, or something really interesting in compilers/compilers research for the first 5 mins of class." ✓
- "I found the design process to be the most difficult. It's not coming up with a solution that was difficult, but what solution will be best and scalable for future homework"



## Questions

- ① DSA TUR - terminology ✓
- ② caller-save registers (handling calls) ✓
- ③ Where does register assignment actually happen? ✓
- ④ What libraries and graphs are it? ✓
- ⑤ Parse tables



















